

IN THE SPECIFICATION

Please replace the paragraph bridging pages 6 and 7 with the following:

Figure 8 shows the principle of the phase-control loop 18 (see FIGS. 5, 6 and 7). It derives the output frequency occurring at an output 30 of a voltage-controlled oscillator 31 from the reference frequency occurring on the lead 34, i.e. at terminal A, with phase-locking and frequency-locking. To develop the phase-locked loop, a divider 35, a phase comparator 33 and a loop filter 32 are provided adjacent to the voltage-controlled oscillator 31.